

IO-Link Device Stack V1.0 release 3 on STM32 Microcontroller

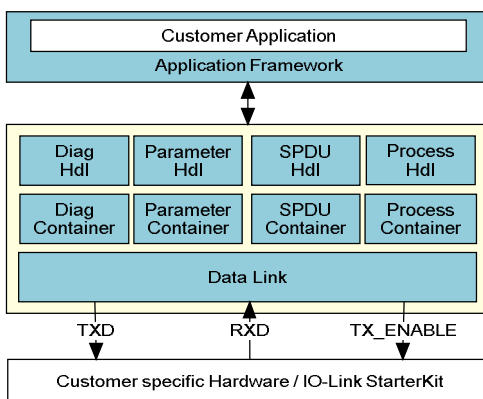
Overview

The IO-Link Device Stack V1.0 release 3 provides the developer of an IO-Link device full access to all features and services defined in the IO-Link specification **version 1.0**.

The firmware also supports the as optional defined features like service PDU, interleave mode and diagnostics handling with event details. Due to its modular design, the library could be adapted to any IO-Link hardware platform. The firmware stack is tested for interoperability in the IO-Link workgroup.



Block Diagram



Features

- IO-Link device firmware in source code according to IO-Link specification version 1.0
- SPDU transport for more than 32 byte parameter data
- Proceeding interleave for more than 2 byte process data
- Supports all IO-Link data channels
- Full diagnostic handling with event details
- All IO-Link frame types supported
- Supported baudrates: 230K4, 34K8, 4K8
- Modular design, performance optimized architecture
- Code coverage tested
- Detailed development documentation with description for portation to different hardware platforms
- Minimal resources: 300 Byte RAM / 6300 Byte ROM (source)
Interleave mode: +280 Byte RAM / + 440 Byte ROM;
SPDU: + 650 Byte RAM / + 3250 Byte ROM;
Event details: + 20 Byte RAM / + 850 Byte ROM
(Not including STM32 firmware library, startup code + application)

Delivery Content

- IO-Link Device Firmware Stack V1.0 release 3
- CD ROM with documented source code written in ANSI –C
- Sample main () application
- Software development documentation with description of hardware adaptation
- Compiler & linker setup example
- Full company buyout license

Related options

- Customer specific hardware and firmware development
- Product development for IO-Link master and device
- Fieldbus implementation
- Stack, Starterkit and evaluation boards for several microcontroller platforms
- IO-Link conformance tests

Order Information

No. 26000.04