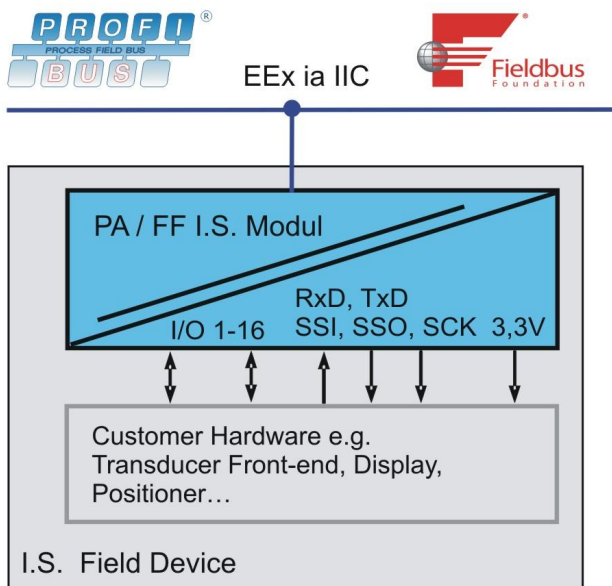


# PROFIBUS PA<sup>®</sup> / FOUNDATION FIELDBUS<sup>®</sup> I.S. Hardware Design Package (H1-IS)

## Overview

The PROFIBUS PA/Foundation Fieldbus I.S. Hardware Design Package includes the schematics and development documentation for a small size universal fieldbus interface. Together with an application specific analog front-end (e.g. for transducers) a complete fieldbus powered intrinsic safe product can be build. Powerful internal CPU and plenty of resources to integrate complex signal conditioning, PROFIBUS PA Profiles or LAS function for Foundation Fieldbus.

## Block Diagram



## Delivery Content

- Circuit Schematic, Bill Of Materials (BOM)
- HW + I.S Design Document incl. Calculations
- Test Documents
- 1 day support during implementation
- 1 Evaluation Board

## Technical Data

- Fieldbus Interface IEC 61158-2
- Current Consumption  
PROFIBUS PA < 11,4 mA  
Foundation Fieldbus < 13,6 mA
- Internal Fault Disconnect Equipment
- Galvanic Isolation 500V DC
- Operating Temperature: -40...+85 °C
- Designed according to ATEX 95 (IEC 60079-0, IEC 60079-11)
- Conform to the FISCO Model
- I.S. Specification:  
EEx ia IIC T4/T6 with  
T4: TA = -40...80 °C  
T6: TA = -40...50 °C  
Ui ≤ 24 V  
Ii ≤ 380 mA  
Pi ≤ 5,32 W
- Supply to Customer HW:  
3,3 V / 3,5 mA
- Renesas M16C62 Microcontroller
- 256kB Flash, 256kB RAM, 8kB EEPROM
- SPC4-2 Fieldbus Controller
- Synchron. / Asynchron. Serial Interface
- 16 I/O (3 Interrupt)
- Connector 1,27mm Pitch PCB Pinheader
- Completely Encapsulated
- Size (LxWxH): 75 x 40 x 14 mm
- Lead free

## Related Services and Products

- Implementation of HW and SW for PROFIBUS PA/Foundation Fieldbus
- Implementation of certain specific application interfaces
- Test: Slave device conformance tests in fieldbus laboratory

## Order Information

No. 42001